



$A_0 \rightarrow$ digital in 0
 $A_1 \rightarrow$ digital in 1
 $A_2 \rightarrow$ digital in 2
 $A_3 \rightarrow$ digital in 3

Second multiplexer:

$A_0 \rightarrow$ digital in 4
 $A_1 \rightarrow$ " " 5
 $A_2 \rightarrow$ " " 6
 $A_3 \rightarrow$ digital in 7

First multiplexer Z goes into Analog 0
 second multiplexer Z goes into Analog 1

	multiplexer #1	multiplexer #2
Y_0 to Y_{15}	independent inputs/outputs	independent inputs/outputs
A_0 to A_3	address inputs	address inputs
\bar{E}	enable input (active LOW)	enable input (active LOW)
Z	common input/output	common input/output
V_{DD}	5V	5V
V_{SS}	ground	ground
	to washer Dig. 0 \rightarrow Dig. 3 ground	to washer Dig. 4 \rightarrow Dig. 7 ground
	Analog in 0	Analog in 1

